

Fig.1 Overview of a Printed Circuit Board Product Life Cycle

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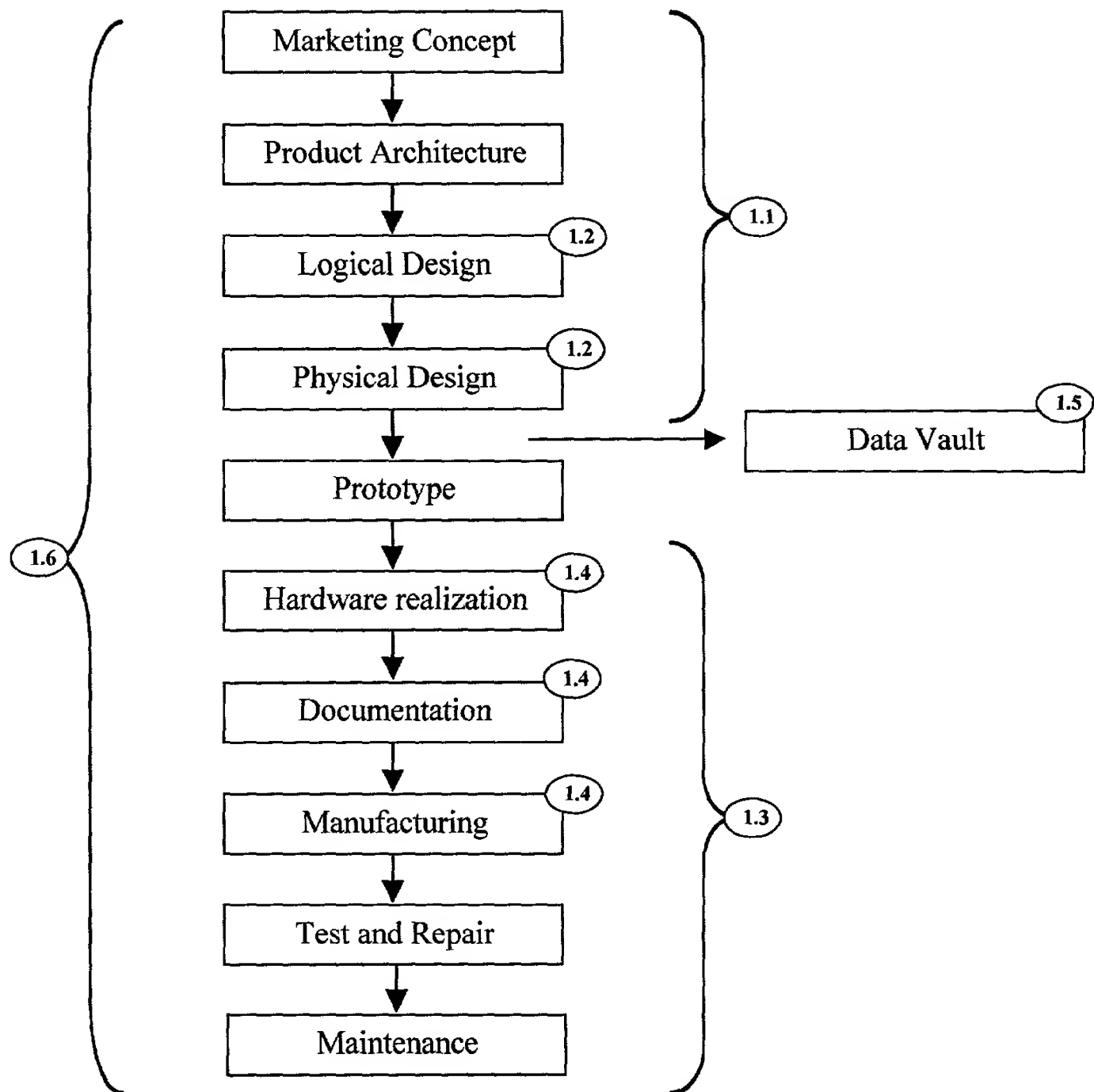


Fig.2 Hardware Packaging Hierarchy of an Electronic Product

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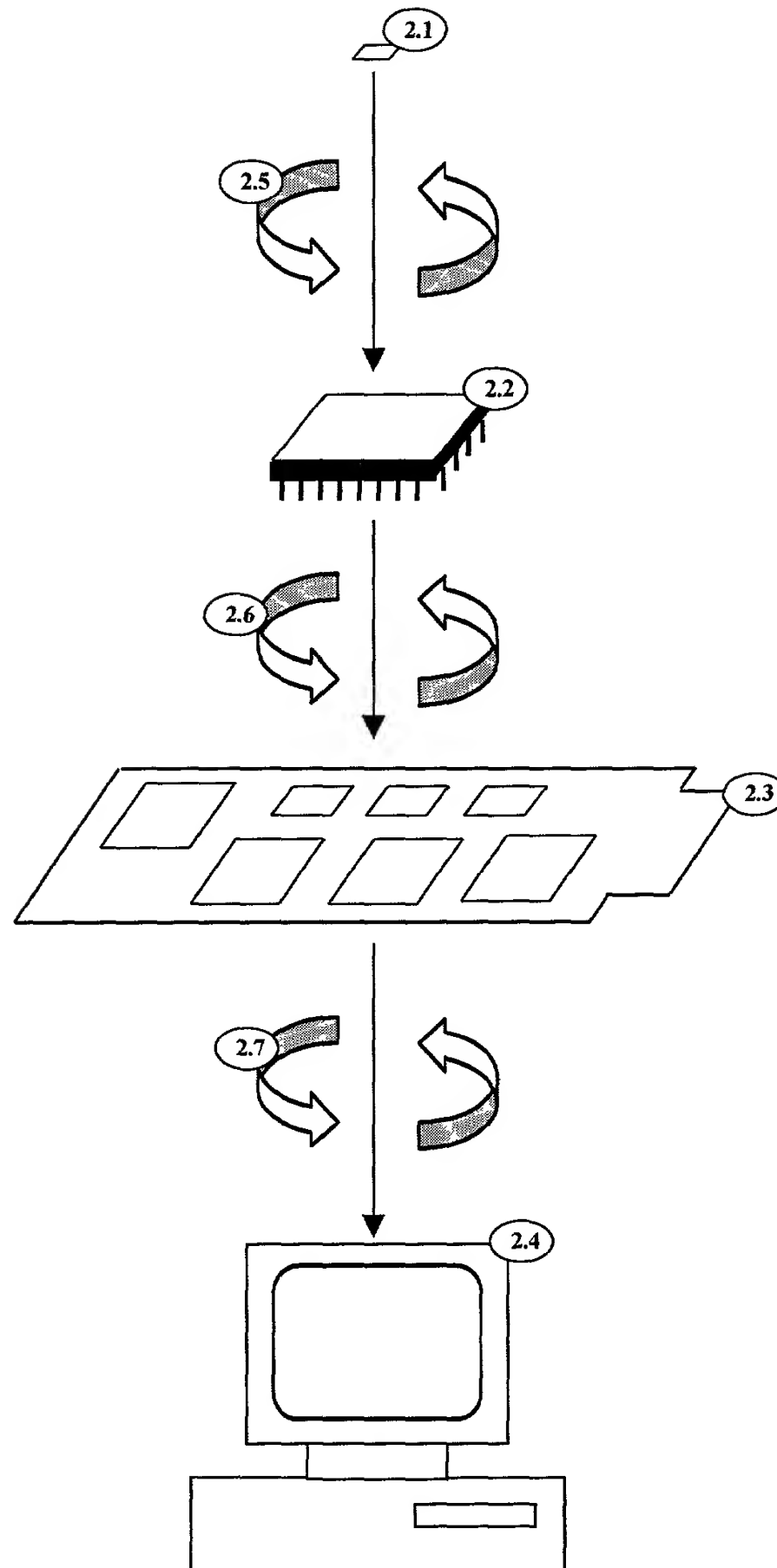


Fig.3A Architecture of the Tool

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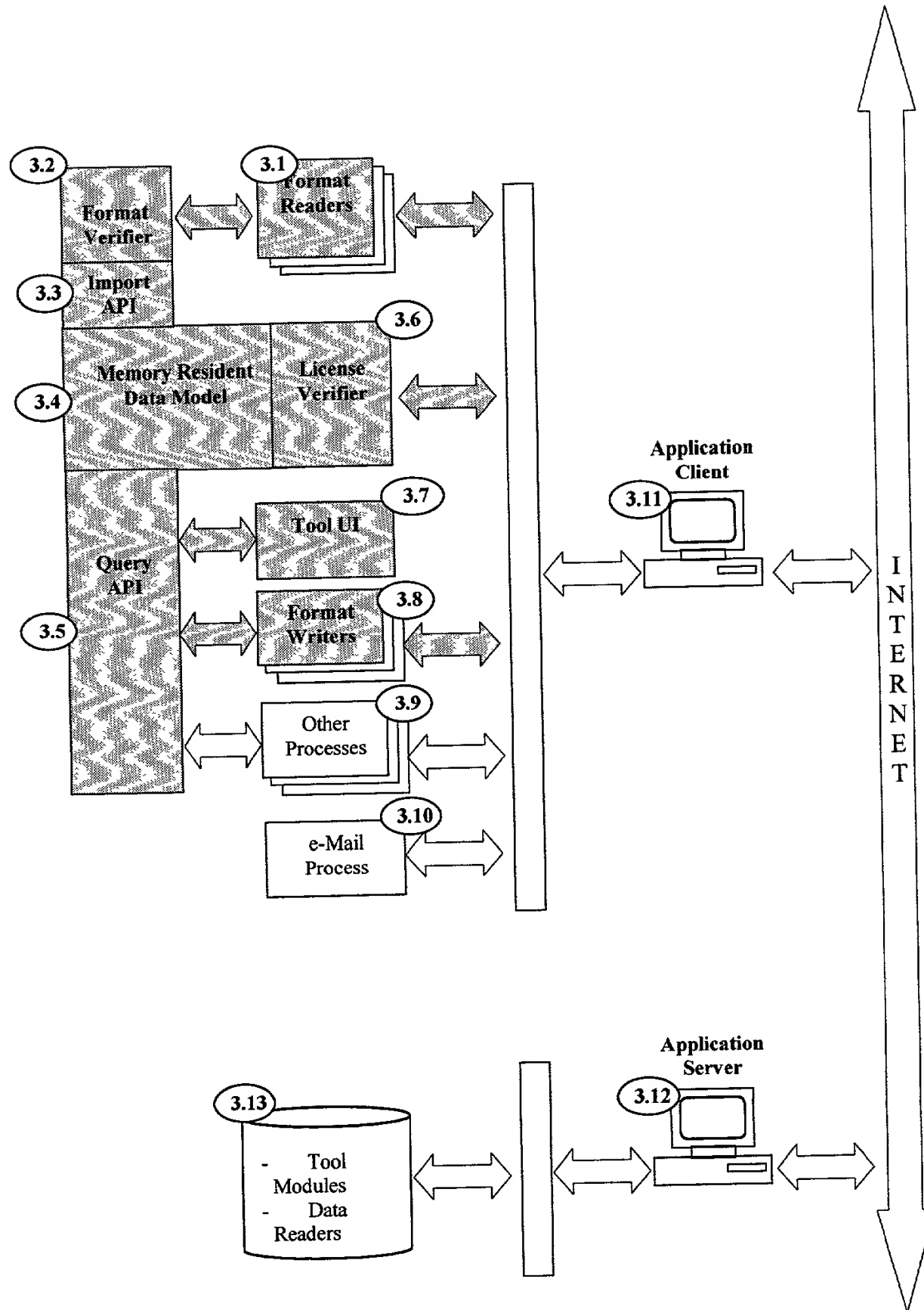


Fig.3B Architecture of the Tool

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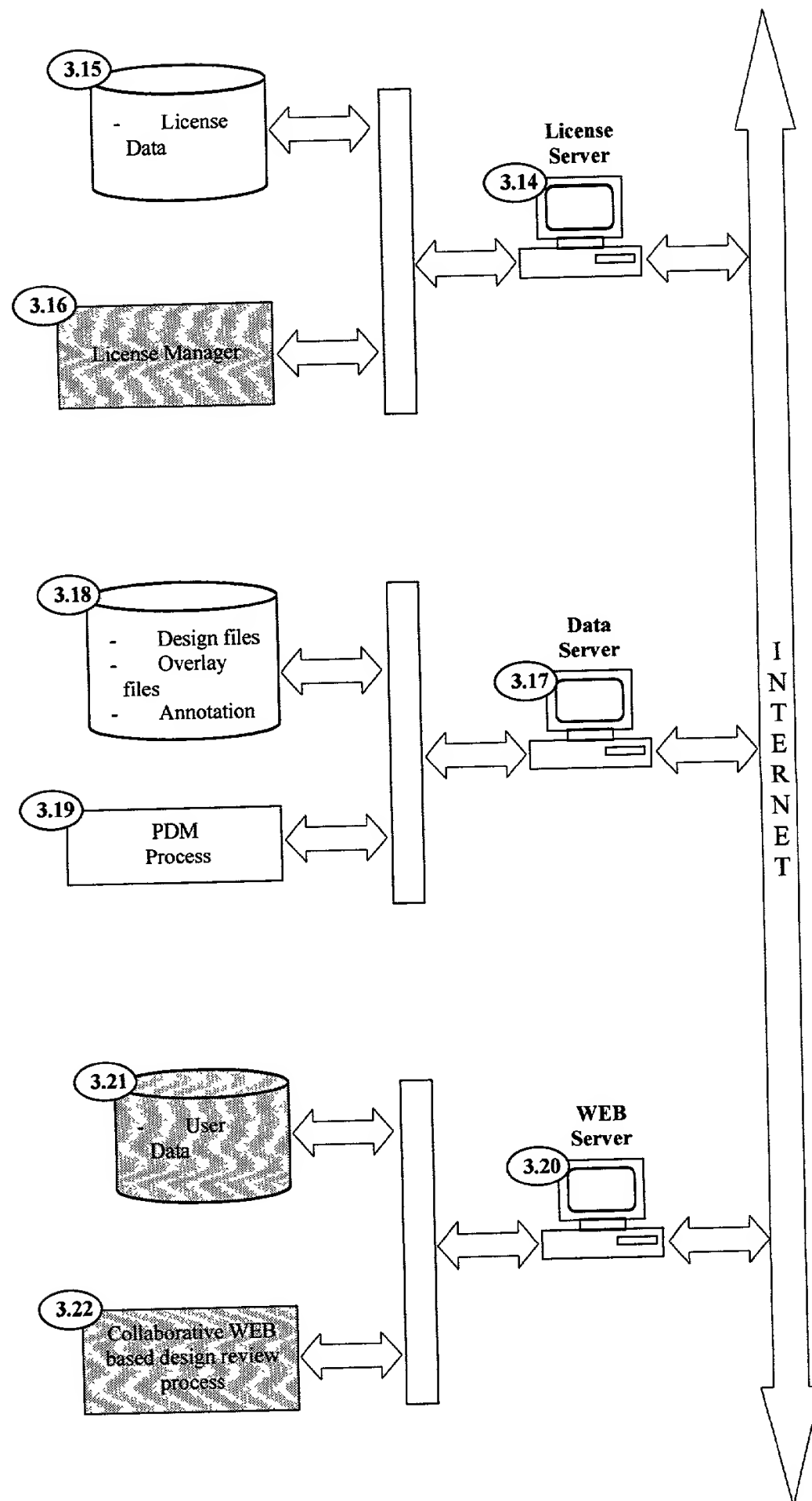


Fig.4A Tool Process Flow

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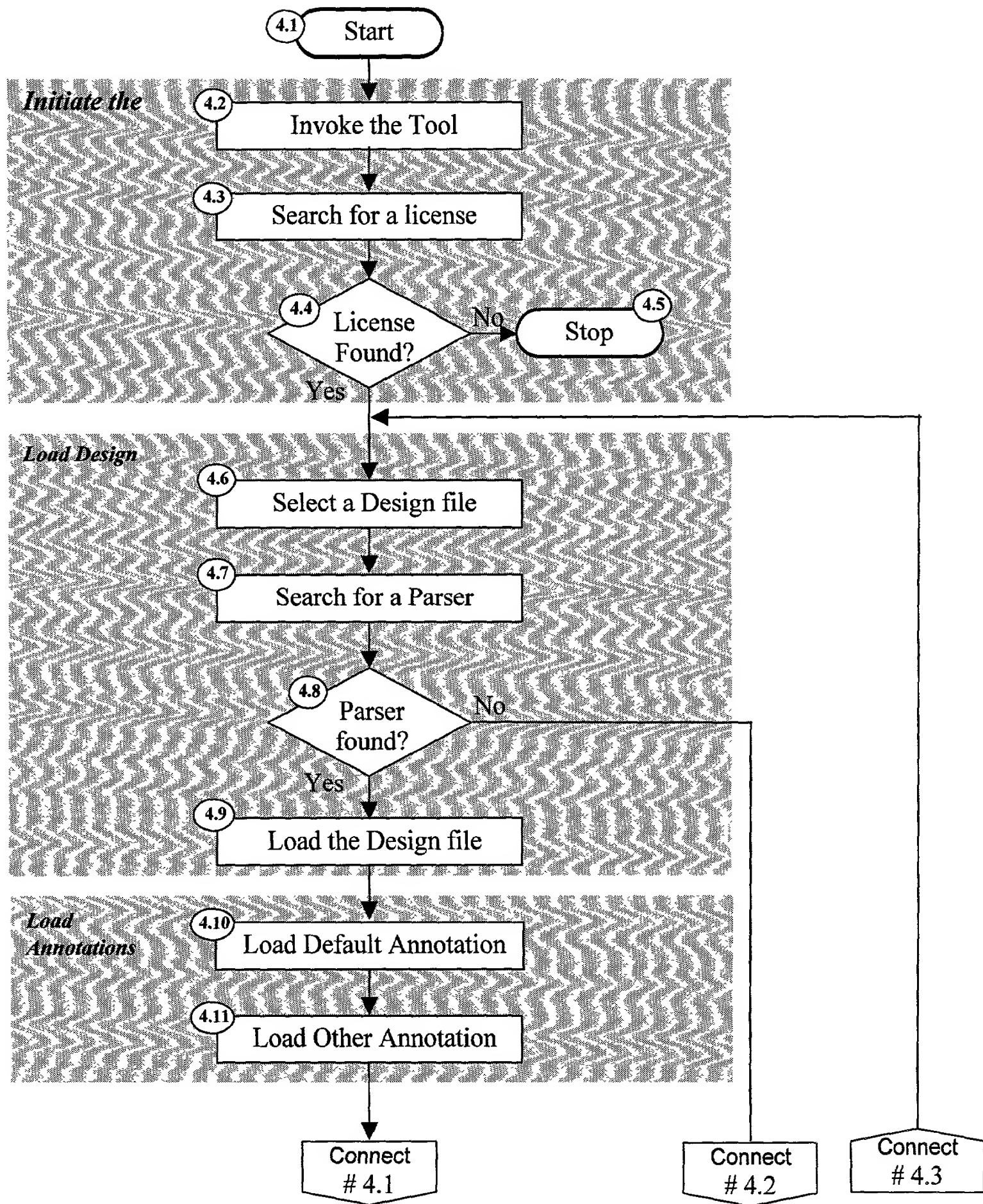


Fig.4B Tool Process Flow
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